

Chapter 8 Semiconductor Memories

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Nanoscale Semiconductor Memories Santosh K. Kurinec
2017-07-28 Nanoscale memories are used everywhere. From your iPhone to a supercomputer, every electronic device contains at least one such type. With coverage of current and prototypical technologies, *Nanoscale Semiconductor Memories: Technology and Applications* presents the latest research in the field of nanoscale memories technology in one place. It also covers a myriad of applications that nanoscale memories technology has enabled. The book begins with coverage of SRAM, addressing the design challenges as the technology scales, then provides design strategies to mitigate radiation induced upsets in SRAM. It discusses the current state-of-the-art DRAM technology and the need to develop high

performance sense amplifier circuitry. The text then covers the novel concept of capacitorless 1T DRAM, termed as Advanced-RAM or A-RAM, and presents a discussion on quantum dot (QD) based flash memory. Building on this foundation, the coverage turns to STT-RAM, emphasizing scalable embedded STT-RAM, and the physics and engineering of magnetic domain wall "racetrack" memory. The book also discusses state-of-the-art modeling applied to phase change memory devices and includes an extensive review of RRAM, highlighting the physics of operation and analyzing different materials systems currently under investigation. The hunt is still on for universal memory that fits all the requirements of an "ideal memory" capable of high-density storage, low-power operation, unparalleled speed,

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high endurance, and low cost. Taking an interdisciplinary approach, this book bridges technological and application issues to provide the groundwork for developing custom designed memory systems.

Handbook of Semiconductor and Bubble Memories Walter A. Triebel 1982

Semiconductor Memories 1980

Semiconductor Memories

Betty Prince 1996-08-06

Provides the reader with memory fundamentals as well as directions for future research. Examines memory history, current memory technology and offers a glimpse at the future of memories. Topics include memory market trends and their importance to both industry and government applications requirements which determine the direction of memory development and technical aspects of memories. Descriptions and usage of the various types of memories including SRAMs, DRAMs, VDRAMs, EPROMs, EEPROMs, and ROMs are also provided.

Finally, it looks at memory packing, large scale integration, and the manufacture of memories. *Advanced Semiconductor Memories* Ashok K. Sharma 2002-10-14 A valuable reference for the most vital microelectronic components in the marketplace DRAMs are the technology drivers of high volume semiconductor fabrication processes for new generation products that, in addition to computer markets, are finding increased usage in automotive, aviation, military and space, telecommunications, and wireless industries. A new generation of high-density and high-performance memory architectures evolving for mass storage devices, including embedded memories and nonvolatile flash memories, are serving a diverse range of applications. Comprehensive and up to date, *Advanced Semiconductor Memories: Architectures, Designs, and Applications* offers professionals in the semiconductor and related

industries an in-depth review of advanced semiconductor memories technology developments. It provides details on: Static Random Access Memory technologies including advanced architectures, low voltage SRAMs, fast SRAMs, SOI SRAMs, and specialty SRAMs (multiport, FIFOs, CAMs) High Performance Dynamic Random Access Memory-DDRs, synchronous DRAM/SGRAM features and architectures, EDRAM, CDRAM, Gigabit DRAM scaling issues and architectures, multilevel storage DRAMs, and SOI DRAMs Applications-specific DRAM architectures and designs - VRAMs, DDR SGRAMs, RDRAMs, SLDRAMs, 3-D RAM Advanced Nonvolatile Memory designs and technologies, including floating gate cell theory, EEPROM/flash memory cell design, and multilevel flash FRAMs and reliability issues Embedded memory designs and applications, including cache, merged processor, DRAM architectures, memory cards,

and multimedia applications Future memory directions with megabytes to terabytes storage capacities using RTDs, single electron memories, etc. A continuation of the topics introduced in Semiconductor Memories: Technology, Testing, and Reliability, the author's earlier work, Advanced Semiconductor Memories: Architectures, Designs, and Applications offers a much-needed reference to the major developments and future directions of advanced semiconductor memory technology.

Semiconductor Memory technology, Testing, And Reliability Ashok K. Sharma 2004

Rad-hard Semiconductor Memories Cristiano Calligaro 2022-09-01 Rad-hard Semiconductor Memories is intended for researchers and professionals interested in understanding how to design and make a preliminary evaluation of rad-hard semiconductor memories, making leverage on standard

CMOS manufacturing processes available from different silicon foundries and using different technology nodes. In the first part of the book, a preliminary overview of the effects of radiation in space, with a specific focus on memories, will be conducted to enable the reader to understand why specific design solutions are adopted to mitigate hard and soft errors. The second part will be devoted to RHBD (Radiation Hardening by Design) techniques for semiconductor components with a specific focus on memories. The approach will follow a top-down scheme starting from RHBD at architectural level (how to build a rad-hard floor-plan), at circuit level (how to mitigate radiation effects by handling transistors in the proper way) and at layout level (how to shape a layout to mitigate radiation effects). After the description of the mitigation techniques, the book enters in the core of the topic covering SRAMs (synchronous, asynchronous, single port and

dual port) and PROMs (based on AntiFuse OTP technologies), describing how to design a rad-hard flash memory and fostering RHBD toward emerging memories like ReRAM. The last part will be a leap into emerging memories at a very early stage, not yet ready for industrial use in silicon but candidates to become an option for the next wave of rad-hard components. Technical topics discussed in the book include: Radiation effects on semiconductor components (TID, SEE) Radiation Hardening by Design (RHBD) Techniques Rad-hard SRAMs Rad-hard PROMs Rad-hard Flash NVMs Rad-hard ReRAMs Rad-hard emerging technologies

A User's Handbook of Semiconductor Memories

Eugene R Hnatek 1977

Handbook of Semiconductor Memories Michael M. Cirovic 1981

Semiconductor Memories 1972

Semiconductor Memory Design and Application

Gerald Luecke 1973

Semiconductor Memories and

Systems Andrea Redaelli
2022-06-07 Semiconductor
Memories and Systems
provides a comprehensive
overview of the current state of
semiconductor memory at the
technology and system levels.
After an introduction on
market trends and memory
applications, the book focuses
on mainstream technologies,
illustrating their current status,
challenges and opportunities,
with special attention paid to
scalability paths. Technologies
discussed include static
random access memory
(SRAM), dynamic random
access memory (DRAM), non-
volatile memory (NVM), and
NAND flash memory.
Embedded memory and
requirements and system level
needs for storage class memory
are also addressed. Each
chapter covers physical
operating mechanisms,
fabrication technologies, and
the main challenges to
scalability. Finally, the work
reviews the emerging trends
for storage class memory,
mainly focusing on the
advantages and opportunities

of phase change based memory
technologies. - Features
contributions from experts
from leading companies in
semiconductor memory -
Discusses physical operating
mechanisms, fabrication
technologies and paths to
scalability for current and
emerging semiconductor
memories - Reviews primary
memory technologies,
including SRAM, DRAM, NVM
and NAND flash memory -
Includes emerging storage
class memory technologies
such as phase change memory

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