

Rabaey Digital Integrated Circuits Chapter 12

Design of High-Performance Microprocessor Circuits Anantha Chandrakasan 2001 The authors present readers with a compelling, one-stop, advanced system perspective on the intrinsic issues of digital system design. This invaluable reference prepares readers to meet the emerging challenges of the device and circuit issues associated with deep submicron technology. It incorporates future trends with practical, contemporary methodologies.

Micro-Relay Technology for Energy-Efficient Integrated Circuits Hei Kam 2014-10-16 This volume describes the design of relay-based circuit systems from device fabrication to circuit micro-architectures. This book is ideal for both device engineers as well as circuit system designers, and highlights the importance of co-design across design hierarchies when trying to optimize system performance (in this case, energy-efficiency). The book will also appeal to researchers and engineers focused on semiconductor, integrated circuits, and energy efficient electronics.

Low-Voltage/Low-Power Integrated Circuits and Systems Edgar Sánchez-Sinencio 1999-01-13 Electrical Engineering Low-Voltage/Low-Power Integrated Circuits and Systems Low-Voltage Mixed-Signal Circuits Leading experts in the field present this collection of original contributions as a practical approach to low-power analog and digital circuit theory and design, illustrated with important applications and examples. Low-Voltage/Low-Power Integrated Circuits and Systems features comprehensive coverage of the latest techniques for the design, modeling, and characterization of low-power analog and digital circuits. Low-Voltage/Low-Power Integrated Circuits and Systems will help you improve your understanding of the trade-offs between analog and digital circuits and systems. It is an invaluable resource for enhancing your designs. This book is intended for senior and graduate students. It is also intended as a key reference for designers in the semiconductor and

communication industries. Highlighted applications include: Low-voltage analog filters Low-power multiplierless YUV to RGB based on human vision perception Micropower systems for implantable defibrillators and pacemakers Neuromorphic systems Low-power design in telecom circuits

Low-Power CMOS Circuits Christian Piguet 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high-performance chips and portable devices. In chapters drawn from Piguet's recently published Low-Power Electronics Design, Low-Power CMOS Circuits: Technology, Logic Design, and CAD Tools addresses the design of low-power circuitry in deep submicron technologies. It provides a focused reference for specialists involved in designing low-power circuitry, from transistors to logic gates. The book is organized into three broad sections for convenient access. The first examines the history of low-power electronics along with a look at emerging and possible future technologies. It also considers other technologies, such as nanotechnologies and optical chips, that may be useful in designing integrated circuits. The second part explains the techniques used to reduce power consumption at low levels. These include clock gating, leakage reduction, interconnecting and communication on chips, and adiabatic circuits. The final section discusses various CAD tools for designing low-power circuits. This section includes three chapters that demonstrate the tools and low-power design issues at three major companies that produce logic synthesizers. Providing detailed examinations contributed by leading experts, Low-Power CMOS Circuits: Technology, Logic Design, and CAD Tools supplies authoritative information on how to design and model for high performance with low power consumption in modern integrated circuits. It is a must-read for anyone designing modern computers or embedded systems.

Silicon Optoelectronic Integrated Circuits Horst Zimmermann

2019-01-30 This book reviews various topics in optoelectronics and the design of microelectronic circuits. It introduces readers to the essential features of optical absorption and device physics of photodetectors, as well as their integration in modern CMOS and BiCMOS technologies. This information provides the basis for understanding the underlying mechanisms of Optoelectronic Integrated Circuits (OEICs), which are described in the main part of the book. In the second edition of this book, new and outstanding integrated high-bandwidth pin photodiodes as well as avalanche photodiodes in the linear mode and in the Geiger mode are introduced. To cover the topic comprehensively, the book presents detailed descriptions of OEICs for a wide range of applications: from various optical sensors, smart sensors, image sensors, 3D-sensors and optical storage systems, to fiber receivers and receivers for optical wireless communication, as well as single-photon detection. This new edition also reflects the latest trends in OEIC research on integrated optical receivers at the quantum limit and electronic-photon integration, and highlights outstanding 3D-integrated application examples like a multi-node optical switch, an optical transceiver, and a high-resolution 3D sensor.

Nanoelectronic Mixed-Signal System Design Saraju Mohanty 2015-02-20

Covering both the classical and emerging nanoelectronic technologies being used in mixed-signal design, this book addresses digital, analog, and memory components. Winner of the Association of American Publishers' 2016 PROSE Award in the Textbook/Physical Sciences & Mathematics category. Nanoelectronic Mixed-Signal System Design offers professionals and students a unified perspective on the science, engineering, and technology behind nanoelectronics system design. Written by the director of the NanoSystem Design Laboratory at the University of North Texas, this comprehensive guide provides a large-scale picture of the design and manufacturing aspects of nanoelectronic-based systems. It features dual coverage of mixed-signal circuit and system design, rather than just digital or analog-only. Key topics such as process variations, power dissipation, and security aspects of electronic system design are discussed. Top-down analysis of all stages--from

design to manufacturing Coverage of current and developing nanoelectronic technologies--not just nano-CMOS Describes the basics of nanoelectronic technology and the structure of popular electronic systems Reveals the techniques required for design excellence and manufacturability

Integrated Circuits for Wireless Communications Asad A. Abidi

1998-12-24 Electrical Engineering Integrated Circuits for Wireless Communications High-frequency integrated circuit design is a booming area of growth that is driven not only by the expanding capabilities of underlying circuit technologies like CMOS, but also by the dramatic increase in wireless communications products that depend on them. Integrated Circuits for Wireless Communications includes seminal and classic papers in the field and is the first all-in-one resource to address this increasingly important topic. Internationally known and highly regarded in the field, editors Asad Abidi, Paul Gray, and Robert G. Meyer have meticulously compiled more than 100 papers and articles covering the very latest high-level integrated circuits techniques and solutions in use today. Integrated Circuits for Wireless Communications is devised expressly to provide IC design engineers, system architects, and integrators with a practical understanding of subjects ranging from architecture choices for integrated transceivers to actual circuit designs in all viable IC technologies, such as bipolar, CMOS, and GaAs. The papers selected represent a breadth of coverage and level of expertise that is simply unmatched in the field. Topics covered include: Radio architectures Receivers Transmitters and transceivers Power amplifiers and RF switches Oscillators Passive components Systems applications **Optimum Partitioning of Analog and Digital Circuitry in Mixed-signal Circuits for Signal Processing** Ken A. Nishimura 1993 *VLSI Design Techniques for Analog and Digital Circuits* Randall L. Geiger 1990

Minimizing and Exploiting Leakage in VLSI Design Nikhil

Jayakumar 2009-12-02 Power consumption of VLSI (Very Large Scale Integrated) circuits has been growing at an alarmingly rapid rate. This increase in power consumption, coupled with the increasing demand for

portable/hand-held electronics, has made power consumption a dominant concern in the design of VLSI circuits today. Traditionally, dynamic (switching) power has dominated the total power consumption of an IC. However, due to current scaling trends, leakage power has now become a major component of the total power consumption in VLSI circuits. Leakage power reduction is especially important in portable/hand-held electronics such as cell-phones and PDAs. This book presents two techniques aimed at reducing leakage power in digital VLSI ICs. The first technique reduces leakage through the selective use of high threshold voltage sleep transistors. The second technique reduces leakage by applying the optimal Reverse Body Bias (RBB) voltage. This book also shows readers how to turn the leakage problem into an opportunity, through the use of sub-threshold logic.

Wireless Technologies Krzysztof Iniewski 2017-12-19 Advanced concepts for wireless technologies present a vision of technology that is embedded in our surroundings and practically invisible. From established radio techniques like GSM, 802.11 or Bluetooth to more emerging technologies, such as Ultra Wide Band and smart dust motes, a common denominator for future progress is the underlying integrated circuit technology. *Wireless Technologies* responds to the explosive growth of standard cellular radios and radically different wireless applications by presenting new architectural and circuit solutions engineers can use to solve modern design problems. This reference addresses state-of-the-art CMOS design in the context of emerging wireless applications, including 3G/4G cellular telephony, wireless sensor networks, and wireless medical application. Written by top international experts specializing in both the IC industry and academia, this carefully edited work uncovers new design opportunities in body area networks, medical implants, satellite communications, automobile radar detection, and wearable electronics. The book is divided into three sections: wireless system perspectives, chip architecture and implementation issues, and devices and technologies used to fabricate wireless integrated circuits. Contributors address key issues in the development of future silicon-based systems, such as scale of integration, ultra-low

power dissipation, and the integration of heterogeneous circuit design style and processes onto one substrate. Wireless sensor network systems are now being applied in critical applications in commerce, healthcare, and security. This reference, which contains 25 practical and scientifically rigorous articles, provides the knowledge communications engineers need to design innovative methodologies at the circuit and system level.

Adaptive Cooling of Integrated Circuits Using Digital Microfluidics Philip Y. Paik 2007 Thanks to increasing power consumption and component density, localized hot spots are becoming a serious challenge in IC (integrated circuit) chip design - so serious, in fact, that Intel recently had to yank a circuit because it was literally burning. For IC engineers grappling with high power dissipation and thermal issues, new droplet-based cooling techniques using digital microfluidics technology could provide the solution. This definitive guide paves the way, with design and implementation methodologies and prototypes for utilizing this groundbreaking technology. After reviewing cooling principles and current bulk cooling methods, the book brings engineers up to speed on emerging droplet-based architectures. Amply illustrated, this milestone work will prove invaluable in tackling IC heat issues that existing methods can no longer address.

Integrated Circuit Design for High-speed Frequency Synthesis

John W. M. Rogers 2006 Get hands-on expertise in the design of frequency synthesizers in high-speed integrated circuits with this complete, one-stop resource packed with straight-from-the-lab techniques, procedures, and applications. It delivers a definitive introduction to system architecture and behavioral analysis. Moreover, you find detailed circuit implementation guidance for state-of-the-art synthesizer designs, emphasizing phase-locked loop-based analog synthesizers and direct digital synthesizers and their applications in CMOS and BiCMOS technologies.

Low Power Design Essentials Jan Rabaey 2009-04-21 *Low Power Design Essentials* contains all the topics of importance to the low power designer. The book lays the foundation with background chapters

entitled “Advanced MOS Transistors and Their Models” and “Power Basics”. These chapters are followed by chapters on the design process including: optimization, architecture and algorithm level, memory, run time, standby logic, and standby memory. Chapters on special topics are also included: power management and modal design, ultra low power, and low power design methodology and flows. The book concludes with a chapter on case studies as well as a chapter on “Projection into the Future”. These chapters are all based on the extensive amount of teaching that the author has carried out both at universities and companies worldwide. All chapters have been drawn up specifically for self-study. They aim, however, at different levels of understanding. All the chapters start with elementary material, but most also contain advanced material.

High-performance Pipeline A/D Converter Design in Deep-submicron CMOS Yun Chiu 2004

Proceedings of the ... ACM Great Lakes Symposium on VLSI. 2004 Characterization and Application of Advanced VLSI Interconnects Weiping Ni 2007

Introduction to VLSI Circuits and Systems John P. Uyemura 2002 CD-ROM contains: AIM SPICE (from AIM Software) -- Micro-Cap 6 (from Spectrum Software) -- Silos III Verilog Simulator (from Simucad) -- Adobe Acrobat Reader 4.0 (from Adobe).

Java/Jini Technologies and High-performance Pervasive Computing Ken Arnold 2002

CMOS/BiCMOS ULSI Kiat Seng Yeo 2002 For upper level and graduate level Electrical and Computer Engineering courses in Integrated Circuit Design as well as professional circuit designers, engineers and researchers working in portable wireless communications hardware. This book presents the fundamentals of Complementary Metal Oxide Semiconductor (CMOS) and Bipolar compatible Complementary Metal Oxide Semiconductor (BiCMOS) technology, as well as the latest technological advances in the field. It discusses the concepts and techniques of new integrated circuit design for building high performance and low power circuits and systems for current and future

very-large-scale-integration (VLSI) and giga-scale-integration (GSI) applications. CMOS/BiCMOS ULSI: Low-Voltage Low-Power is an essential resource for every professional moving toward lower voltage, lower power, and higher performance VLSI circuits and subsystems design.

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promises.

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