

Reti Logiche E Calcolatore 2nd Edition

Progetti di reti logiche Bruno Ciciani 1986
Appunti di reti logiche e calcolatori numerici
Enzo Belardinelli 1967
Esercizi di reti logiche Eugenio Faldella 1975
Reti logiche. Introduzione alla teoria e alla progettazione Michele Angelaccio 2006
Reti logiche e calcolatore Fabrizio Luccio 1991
Reti logiche. Esercizi commentati e risolti A. Benso 2020-07-01 Il presente volume è principalmente rivolto agli studenti che frequentano corsi di Reti Logiche sia presso i corsi di Laurea sia presso corsi di Diploma della Facoltà di Ingegneria. In particolare, tutti i testi proposti sono tratti da compiti di esame assegnati negli ultimi anni presso il Politecnico di Torino nei corsi di Reti Logiche per il Corso di Laurea in Ingegneria Informatica, per il Diploma Universitario in Ingegneria Informatica (sede di Ivrea) e per il Diploma Universitario a distanza del Consorzio Nettuno (poli di Torino ed Alessandria). Il testo fa riferimento, per quanto riguarda la terminologia adottata e le metodologie impiegate, a quanto illustrato nel volume "Reti Logiche" (F. Corno e P. Prinetto, ed. Levrotto e Bella, Torino, 1995) e presenta, per la maggior parte degli esercizi proposti, sia una possibile soluzione sia un commento alla medesima. Gli esercizi riguardano la progettazione, secondo diverse metodologie di sintesi, di circuiti combinatori e sequenziali. Parecchi esercizi sono svolti in modi diversi, offrendo le soluzioni ritenute via via più appropriate alla metodologia di sintesi prescelta. A titolo di esempio, nel caso di sintesi manuale, particolare enfasi viene data a realizzazioni al livello gerarchico di astrazione RT, in linguaggio VHDL e nel caso di sintesi parzialmente automatica viene fornita una descrizione in un formato accettato dal minimizzatore logico Espresso. Gli autori ringraziano anticipatamente quei lettori che vorranno gentilmente segnalare, ai sottoelencati indirizzi di posta elettronica, sia eventuali inesattezze sia soluzioni alternative a quelle proposte nel testo.
Reti logiche e trattamento dell'informazione. Esercizi e verifiche Ferruccio Barsi 2010

Reti logiche Alberto Del Bimbo 1995
Calcolatori elettronici Arrigo Luigi Frisiani 1979
Appunti di reti logiche Roberto Laschi 2006
Aritmetica, reti logiche e struttura del calcolatore. Con cenni alla realizzazione in Assembler delle principali istruzioni e tipi Pascal Ugo Moscato 1991
Teoria delle reti logiche Fabrizio Luccio 1977
Reti logiche e aritmetica di macchina. Esercizi e test Ferruccio Barsi 2008
Calcolatori elettronici Arrigo Frisiani 1972
Esercizi di reti logiche Michele Angelaccio 1996
Lezioni di calcolatori elettronici Ivo De Lotto 1996
Reti Logiche Roberto Laschi 2020-06-01
Macchine digitali e reti logiche, Codifica delle informazioni - principali codici numerici e alfanumerici, Modelli e metodi di analisi e sintesi delle reti logiche combinatorie e sequenziali: Reti combinatorie algebra della commutazione i principali componenti combinatori: decoder, multiplexer, operatori aritmetici sintesi minima e sintesi basata sulle espressioni generali e sui teoremi di espansione (Multiplexer, LUT e ROM) Reti sequenziali - automi a stati finiti reti sequenziali asincrone e il FF-SR reti sequenziali sincrone e il FF-D reti sequenziali sincrone comunemente impiegate come componenti di sistemi digitali: contatori e registri di vario tipo Composizione di contatori, registri e reti combinatorie programmabili. Introduzione all'architettura di un calcolatore: Data Path e Unità di controllo.
Reti logiche M. Morris Mano 2008
Reti logiche: complementi ed esercizi Bruno Fadini 1995
Dalle porte and or not al sistema calcolatore. Un viaggio nel mondo delle reti logiche Paolo Corsini 1992

Reti Logiche E Calcolatore

2nd Edition

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