

Qpsk Modulator And Demodulator Using Fpga For Sdr

International Conference on Computer Networks and Communication Technologies
S. Smys 2018-09-17 The book features research papers presented at the International Conference on Computer Networks and Inventive Communication Technologies (ICCNCT 2018), offering significant contributions from researchers and practitioners in academia and industry. The topics covered include computer networks, network protocols and wireless networks, data communication technologies, and network security. Covering the main core and specialized issues in the areas of next-generation wireless network design, control, and management, as well as in the areas of protection, assurance, and trust in information security practices, these proceedings are a valuable resource, for

researchers, instructors, students, scientists, engineers, managers, and industry practitioners.

FM Demodulators in Software-defined Radio Using FPGAs with Rapid Prototyping Marc A. Padilla 2011 With the advent of software-defined radio, many radio applications have and are currently being designed for FPGAs, due to their high performance and reconfigurability. Invariably, "legacy" waveforms, such as FM, will need to be supported in such systems. A challenge that comes with programming FPGAs is the increased design and implementation time over conventional software programming. In this thesis, three FM demodulator techniques are implemented and compared in an FPGA. Two techniques are found to have similar SNR performance while having very different FPGA implementation characteristics.

Library based design is explored for demodulators to increase FPGA design productivity. A block library is created and verified by use in tested demodulator designs. Two design tools that aim to increase design productivity in FPGAs, Ogre and HMFlow, are also examined and used to implement FM demodulators in a PCM/FM receiver design. Ogre leverages the demodulator block library, along with accompanying metadata, to decrease design time significantly. Design performance is not sacrificed when using Ogre. HMFlow, which relies on finer-grained blocks, reuses block implementation data to speed up implementation of the full design. The implementation of the HMFlow demodulator design is sped up by 3x but, when compared with the standard flow, produces an implementation with a reduced maximum clock rate (about 1/2) and with slightly more resources (about 6%). When comparing Ogre with HMFlow, the coarser-grained blocks of

Ogre provide a more efficient design experience than that of HMFlow.

Information Hiding Matthias Kirchner 2013-02-20 This book contains the thoroughly refereed post-conference proceedings of the 14th Information Hiding Conference, IH 2012, held in Berkeley, CA, USA, in May 2012. The 18 revised full papers presented were carefully reviewed and selected from numerous submissions. The papers are organized in topical sections on multimedia forensics and counter-forensics, steganalysis, data hiding in unusual content, steganography, covert channels, anonymity and privacy, watermarking, and fingerprinting.

Advances in Biometrics G.R. Sinha 2019-12-13 This book provides a framework for robust and novel biometric techniques, along with implementation and design strategies. The theory, principles, pragmatic and modern methods, and future directions of biometrics are

presented, along with in-depth coverage of biometric applications in driverless cars, automated and AI-based systems, IoT, and wearable devices. Additional coverage includes computer vision and pattern recognition, cybersecurity, cognitive computing, soft biometrics, and the social impact of biometric technology. The book will be a valuable reference for researchers, faculty, and practicing professionals working in biometrics and related fields, such as image processing, computer vision, and artificial intelligence. Highlights robust and novel biometrics techniques Provides implementation strategies and future research directions in the field of biometrics Includes case studies and emerging applications

Design and Implementation of Discrete Multitone Modulator Using FPGA Hadi T. Ziboon 2014-04 The aim of the book is to design, simulation and implementation of the Discrete Multitone (DMT) modulation for ADSL based on Software

Defined Radio (SDR) using FPGA. DMT divides the available bandwidth into parallel sub-channels. The main units of the DMT modulator are serial to parallel converter, symbol mapping, IFFT, parallel to serial and cyclic prefix. There are two techniques to transmit data namely complex transmission and real transmission. Real transmission is preferred for DSL, since it needs single wire but uses twice number of IFFT.

Advances in Computing and Communications, Part III Ajith Abraham 2011-07-08 This volume is the third part of a four-volume set (CCIS 190, CCIS 191, CCIS 192, CCIS 193), which constitutes the refereed proceedings of the First International Conference on Computing and Communications, ACC 2011, held in Kochi, India, in July 2011. The 70 revised full papers presented in this volume were carefully reviewed and selected from a large number of submissions. The papers are organized in topical sections on security,

trust and privacy; sensor networks; signal and image processing; soft computing techniques; system software; vehicular communications networks.

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Demodulator System Man

Kin Tam 2006

M-ary Amplitude Shift

Keying-orthogonal

Frequency Division

Multiplexing Modulator and Demodulator

Implementation Using FPGA

Haiying Deng 2001

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