

# Designing And Implementation Of Smmps Circuits

Proceedings [of The] Power Electronics Design Conference 1985

*Proceedings of the ... International Symposium on Microelectronics* 1997

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*1997 International Symposium on Microelectronics* 1997 Contains the proceedings of IMAPS '97 in Adobe portable document format (PDF).

*Proceedings of the ... International Symposium on Power Semiconductor Devices and ICs* 2004

*Switch-mode Power Supply Design* P. R. K. Chetty 1986

*Electronic Engineering Design* 2002

**High Performance Computing** 2004

**Dissertation Abstracts International** 2004

*Electronic Design* 1999

**IETE Technical Review** 1993

Proceedings Euromicro. Conference 1998

Advanced Designs and Researches for Manufacturing Peng Cheng Wang 2012-12-13 Selected papers from the 2nd International Conference on Materials and Products Manufacturing Technology (ICMPMT 2012) September 22-23, 2012, Guangzhou, China

Electrical & Electronics Abstracts 1997

**Design and Implementation of SMPS Circuit Using PWM Concepts**

Switched Mode Power Supply (SMPS) is the most prevailing architecture for DC power supply in modern systems, primarily for its capability to handle variable loads. Apart from efficiency the size and weight of the power supplies is becoming a great area of concern for the Power Supply Designers. In this thesis an AC to DC converter SMPS circuit, having a power MOSFET for switching operation and a PWM based Feedback circuit for driving the switching of the MOSFET, is designed and

simulated in NI MULTISIM circuit design environment. Further the same circuit is Hardware implemented and tested using NI ELVIS Suite. In this design the line voltage at 220V/50Hz is taken as input, this voltage is stepped down, rectified and passed through filter capacitor to give an unregulated DC voltage. This unregulated voltage is chopped using a MOSFET switch, driven by PWM feedback signal, to control the output voltage level. An Isolation Transformer is used to isolate the DC output from input supply. The transformer output is again rectified by the high frequency Diode bridge rectifier and is filtered using a capacitor to give the regulated DC output. A Voltage regulator is connected to give the precise voltage output. The feedback network generates a high frequency PWM signal to drive the MOSFET switch. The dc voltage at the output depends on the width of the switching pulse. The pulse width is varied with the changes in the DC output voltage level, this change in the pulse width cancels the output voltage change and the SMPS output remains constant irrespective of load variations.

**Optimal Design of Switching Power Supply** Zhanyou Sha 2015-06-15

A contemporary evaluation of switching power design methods with real world applications • Written by a leading author renowned in his field • Focuses on switching power supply design, manufacture and debugging • Switching power supplies have relevance for contemporary applications including mobile phone chargers, laptops and PCs • Based on the authors' successful "Switching Power Optimized Design 2nd Edition" (in Chinese) • Highly illustrated with design examples of real world applications

Fifth European Conference on Power Electronics and Applications: Electronic power supply systems 1993

**Proceedings of the International Conference on Electromagnetic Interference and Compatibility '97, 3-5 December, 1997, Hyderabad, India** 1999

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**Proceedings of the International Conference on Electromagnetic Interference and Compatibility** 2002

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